

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Hong Jiang et al.

Art Unit:

2195

Serial No.:

10/750,583

Examiner:

Eric Charles Wai

Filed:

December 31, 2003

Docket:

ITL.1704US

P17510

For:

Processing Architecture Having

Passive Threads and Active

Assignee:

Intel Corporation

Semaphores

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to the receipt of an office action, please amend the above-referenced patent application as follows:

Date of Depo3it: July 15, 2008

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the

Cynth(a). Hayden